

To: USPTO General

From: Michelle Craig (JJF)

3-15-06 10:34am p. 1 of 21

Berkeley Law &
Technology Group, LLC

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Please find attached for filing in connection with application no. 10/641,371, entitled ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS, the following documents:

- REV/POA
- Statement Under 37 CFR 3.73(b)
- Copy of Assignment of Patent Rights executed 10/31/2005 (18 pages)

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I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on:

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POWER OF ATTORNEY TO PROSECUTE APPLICATIONS BEFORE THE USPTO

I hereby revoke all previous powers of attorney given in the application identified in the attached statement under 37 CFR 3.73(b).

I hereby appoint:

Practitioner(s) associated with the Customer Number:

00043831

OR

Practitioner(s) named below (if more than ten patent practitioners are to be named, then a customer number must be used):

Name	Registration Number	Name	Registration Number

as attorney(s) or agent(s) to represent the undersigned before the United States Patent and Trademark Office (USPTO) in connection with any and all patent applications assigned only to the undersigned according to the USPTO assignment records or assignment documents attached to this form in accordance with 37 CFR 3.78(b).

Please change the correspondence address for the application identified in the attached statement under 37 CFR 3.73(b) to:

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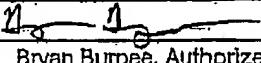
Assignee Name and Address:

Zarbaña Digital Fund LLC
2711 Centerville Road, Suite 400
Wilmington, DE 19808

A copy of this form, together with a statement under 37 CFR 3.73(b) (Form PTO/SB/86 or equivalent) is required to be filed in each application in which this form is used. The statement under 37 CFR 3.73(b) may be completed by one of the practitioners appointed in this form if the appointed practitioner is authorized to act on behalf of the assignee, and must identify the application in which this Power of Attorney is to be filed.

SIGNATURE of Assignee of Record

The individual whose signature and title is supplied below is authorized to act on behalf of the assignee.

Signature		Date	11/13/06
Name	Bryan Burpee, Authorized Person for Zarbaña Digital Fund LLC	Telephone	
Title			

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Zarbaña Digital Fund LLC

Application No./Patent No./Control No.: 10/641,371 Filed/Issue Date: 8/13/2003

Entitled: ADAPTIVE PREDISTORTION FOR A TRANSMIT SYSTEM WITH GAIN, PHASE AND DELAY ADJUSTMENTS

Zarbaña Digital Fund LLC, a Limited Liability Company
(Name of Assignee) (Type of Assignee: corporation, partnership, university, government agency, etc.)

states that it is:

1. the assignee of the entire right, title, and interest; or
2. an assignee of less than the entire right, title and interest
(The extent (by percentage) of its ownership interest is _____ %)

In the patent application/patent identified above by virtue of either:

A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or a true copy of the original assignment is attached.

OR
B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: Aryan Saeed, To: Icefyre Semiconductor Corporation
The document was recorded in the United States Patent and Trademark Office at
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2. From: Icefyre Semiconductor Corporation, To: Icefyre Semiconductor, Inc.
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Reel 017262, Frame 0905, or for which a copy thereof is attached.
3. From: Icefyre Semiconductor, Inc., To: Zarbaña Digital Fund LLC
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

Additional documents in the chain of title are listed on a supplemental sheet.

As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

STJ/M

3/14/06

Date

Steven J. Munson, Reg. No 47,812

503.439.6500

Printed or Typed Name

Telephone Number

Attorney at Law

Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Name (line 1) Zarbaña Digital Fund, LLC

Name (line 2) _____

Address (line 1) 2711 Centerville Road, Suite 400

Address (line 2) _____

Address (line 3) Wilmington DE 09808
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PATENT

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Correspondent Name and Address

Area Code and Telephone Number [617-261-3100]

Name [Patent Administrator]

Address (line 1) [Kirkpatrick & Lockhart Nicholson Graham LLP]

Address (line 2) [75 State Street]

Address (line 3) [Boston, MA 02109-1808]

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Application Number(s) or Patent Number(s)

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Enter either the Patent Application Number or the Patent Number (DO NOT ENTER BOTH numbers for the same property).

Patent Number(s)

Patent Application Number(s)

[10/068,120]	[10/094,826]	[10/154,282]	[10/155,107]	[10/273,908]
[10/377,859]	[10/629,644]	[10/629,640]	[09/918,106]	[10/205,743]
[10/377,860]	[10/613,355]	[10/641,370]	[10/610,454]	[10/613,372]
[10/641,372]	[11/099,916]	[10/610,497]	[10/627,881]	[10/613,856]
[10/641,371]	[10/641,374]	[10/641,373]	[10/661,945]	[10/662,063]
[10/661,943]	[10/661,147]	[10/779,322]	[10/883,170]	[10/884,633]
[10/954,429]	[10/884,627]	[11/268,765]	[60/625,301]	

Patent Cooperation Treaty (PCT)

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only if a U.S. Application

PCT [] PCT [] PCT []

Number
has not been assigned.

PCT [] PCT [] PCT []

Number of Properties

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Fee Amount for Properties Listed (37 CFR 3.41): \$ [1,520.00]

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Statement and Signature

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document. Charges to deposit account are authorized, as indicated herein.

Jason P. Fiorillo, Reg. No. 52,892

Name of Person Signing



Signature

12/23/05

Date

915509_1

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Exhibit B

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, **Icelyfyre Semiconductor, Inc.**, a Delaware Corporation, ("Assignor"), does hereby sell, assign, transfer and convey unto **Zarbaña Digital Fund LLC**, a Delaware limited liability company, having an office at 2711 Centerville Road, Suite 400, Wilmington, New Castle County, DE 19808 ("Assignee"), or its designees, all right, title and interest that exist today and may exist in the future, in and to all of the following (the "Patent Rights"): (a) the provisional patent applications, patent applications and patents listed below; (b) all patents or patent applications to which any of the foregoing claim priority, and (c) current or future rights to (i) provisional patent applications, patent applications, and patents of any kind relating to any inventions and discoveries described in any provisional patent applications, patent applications and patents listed below; (ii) reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, and divisions of such patents and applications; and (iii) foreign counterparts to any of the foregoing, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants; (d) the rights to all inventions and discoveries; (e) rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections or other governmental grants of any type related to the any of the foregoing categories (a), (b), (c) and (d), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement or understanding; (f) causes of action (whether currently pending, filed, or otherwise) and other enforcement rights, including, without limitation, all rights under the provisional patent applications, patent applications and patents listed below and/or under or on account of any of the foregoing categories (b), (c) and/or (d) to

- (i) damages,
- (ii) injunctive relief and
- (iii) other remedies of any kind

for past, current and future infringement; and
(g) all rights to collect royalties and other payments under or on account of any of the foregoing.

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<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-001	Switched-Mode Power Amplifier Integrally Performing Power Combining	Issued	6,603,352	U.S.A.	Wight, James	12/3/2001
ICE-001PC	Switched-Mode Power Amplifier Integrally Performing Power Combining	Nationalized	CA02/01847	PCT	Wight, James	12/3/2002
ICE-001JP	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2003-550250	Japan	Wight, James	12/3/2002
ICE-001KR	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	7008505/2004	Korea	Wight, James	06/03/2004
ICE-001CN	Switched-Mode Power Amplifier Integrally Performing Power Combining	Pending	2824126.6	China	Wight, James	12/3/2001
ICE-001AU**	Switched-Mode Power Amplifier Integrally Performing Power Combining	Lapsed	2002351903	Australia	Wight, James	12/3/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-001CP	Switched-Mode Power Amplifier Integrally Performing Power Combining (CP)	Issued	6,937,096	U.S.A.	Wight, James	6/30/2003
ICE-002PR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Expired	60/307/889	U.S.A.	Wight, James	7/27/01
ICE-002	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	10/068,120	U.S.A.	Wight, James	2/6/2002
ICE-002PC	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Nationalized	CA02/01150	PCT	Wight, James	7/26/2002
ICE-002CA	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Abandoned	2455111	Canada	Wight, James	7/26/2002
ICE-002CN	Reception Diversity Combiner with Selectable Inversion and Variable Gain	Pending	2818192.1	China	Wight, James	7/26/2002
ICE-002EP	Reception Diversity Combiner	Pending	2748525.9	EPO	Wight, James	7/26/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
	with Selectable Inversion and Variable Gain					
ICE-002JP	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned	2003-518082	Japan	Wight, James	7/26/2002
ICE-002KR	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Pending	7001206/2004	Korea	Wight, James	01/27/2004
ICE-002NO	Selectable Inversion/Variable Gain Combiner for Diversity Reception In RF Transceivers	Abandoned but revivable	20040269	Norway	Wight, James	7/26/2002
ICE-003	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Allowed	10/094,826	U.S.A.	Wight, James	3/11/2002
ICE-003PC	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Expired	CA02/01498	PCT	Wight, James	10/4/2002
ICE-003AU**	Pseudo-Noise Carrier Suppression/Image Rejection Up and Down Converters	Lapsed	2002328744	Australia	Wight, James	10/4/2002
ICE-004	Up/Down Conversion Circuitry for Radio Transceiver	Pending	10/154,282	U.S.A.	Binkett, Alexander	5/22/2002

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-004PC	Up/Down Conversion Circuitry for Radio Transceiver	Expired	CA02/01497	PCT	Birkett, Alexander	10/4/2002
ICE-004AU**	Up/Down Conversion Circuitry for Radio Transceiver	Lapsed	2002328743	Australia	Birkett, Alexander	10/4/2002
ICE-005	Oscillator Frequency Offsets	Abandoned	10/155,107	U.S.A.	Birkett, Alexander	5/7/2002
ICE-005PC	Frequency Offset Generator for Synthesized Signals	Expired	CA02/01499	PCT	Birkett, Alexander	10/4/2002
ICE-005AU**	Frequency Offset Generator for Synthesized Signals	Lapsed	2002328745	Australia	Birkett, Alexander	10/4/2002
ICE-006	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	10/273,908	U.S.A.	Parker, Kevin	10/18/2002
ICE-006JP	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Pending	2004-543858	Japan	Parker, Kevin	04/15/2005

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-006AU**	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Lapsed	2003278003	Australia	Parker, Kevin	10/14/2003
ICE-006PC	Phasor Fragmentation Circuitry and Method for Processing Modulated Signals Having Non-Constant Envelopes	Expired	2004036862	PCT	Parker, Kevin	4/29/2004
ICE-007	Systems and Modules for Use with Trellis-Based Decoding	Pending	10/377,859	U.S.A.	Amer, Maher	2/28/2003
ICE-007PC	Viterbi Decoder Operating In Units Of A Plurality Of Transitions	Expired	CA04/000282	PCT	Amer, Maher	2/26/04
ICE-008PR	Parallel Convolutional Encoder	Expired	60/399,728	U.S.A.	Amer, Maher	8/1/2002
ICE-008	Parallel Convolutional Encoder	Pending	10/629,644	U.S.A.	Amer, Maher	7/29/2003
ICE-008KR	Parallel Convolutional Encoder	Pending	7001719/2005	Korea	Amer, Maher	01/31/2005
ICE-008CN	Parallel Convolutional Encoder	Pending	03818236.X	China	Amer, Maher	07/31/2003
ICE-008JP	Parallel Convolutional Encoder	Pending	2004-525088	Japan	Amer, Maher	03/24/2005

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<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-008PC	Parallel Convolutional Encoder	Nationalized	CA03/0113	PCT	Amer, Maher	07/31/03
ICE-008AU**	Parallel Convolutional Encoder	Lapsed	2003249822	Australia	Amer, Maher	7/31/2003
ICE-009PR	Parallel Scrambler Descrambler	Expired	60/411,343	U.S.A.	Amer, Maher	9/18/02
ICE-009	Parallel Scrambler/Descrambler	Pending	10/629,640	U.S.A.	Amer, Maher	7/29/2003
ICE-009PC	Parallel Scrambler/Descrambler	Expired	CA03/01132	PCT	Amer, Maher	7/31/2003
ICE-009AU**	Parallel Scrambler/Descrambler	Lapsed	2003249821	Australia	Amer, Maher	7/31/2003
ICE-010PR	Processing Engines and RF Circuitry for Multi-Carrier Modulation Transceivers	Expired	60/277,941	U.S.A.	Wright, James	3/23/01

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-010	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	09/918,106	U.S.A.	Wright, James	7/30/2001
ICE-010PC	Signal Decomposition for The Control Of its Dynamic Range	Nationalized	CA02/001174	PCT	Wright, James	7/29/2002
ICE-010CA	Signal Decomposition for The Control Of its Dynamic Range	Abandoned but Revivable	2,455,277	Canada	Wright, James	7/29/2002
ICE-010CN	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Pending	20818664.8	China	Wright, James	7/29/2002
ICE-010EP	Signal Decomposition for The Control Of its Dynamic Range	Pending	2748528.3	EPO	Wright, James	7/29/2002
ICE-010JP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned	2003-518144	Japan	Wright, James	7/29/2002
ICE-010KR	Computational Circuits and Methods for Processing Modulated Signals Having	Pending	7001445/2004	Korea	Wright, James	01/30/2004

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<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-010NO	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes	Abandoned but Revivable	20040367	Norway	Wight, James	1/27/2004
ICE-010CP	Computational Circuits and Methods for Processing Modulated Signals Having Non-Constant Envelopes (CIP)	Pending	10/205,743	U.S.A.	Wright, James	7/26/2002
ICE-011	Chireix Architecture Using Low Impedance Amplifiers	Issued	6836183	U.S.A.	Wight, James	10/16/2002
ICE-011JP	Chireix Architecture Using Low Impedance Amplifiers	Pending	2004-543859	Japan	Wight, James	04/15/2005
ICE-011PC	Chireix Architecture Using Low Impedance Amplifiers	Nationalized	CA03/001546	PCT	Wight, James	10/14/2003
ICE-011EP	Chireix Architecture Using Low Impedance Amplifiers	Pending	03769084	EPO	Wight, James	10/14/2003

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<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-011AU**	Chireix Architecture Using Low Impedance Amplifiers	Lapsed	2003278004	Australia	Wight, James	10/14/2003
ICE-012	Memory Systems and Method for Use In Trellis-Based Decoding	Pending	10/377,860	U.S.A.	Amer, Maher	2/28/2003
ICE-013	Predistortion Circuit for a Transmit System	Pending	10/613,355	U.S.A.	Saed, Aryan	7/3/2003
ICE-013CP	Predistortion Circuit for a Transmit System (CIP)	Pending	10/641,370	U.S.A.	Saed, Aryan	8/13/2003
ICE-014	A Method Of and Device for Antennae Diversity Switching	Pending	10/610,454	U.S.A.	Saed, Aryan	6/30/2003
ICE-014PC	A Method Of and Device for Receive Antennae Diversity Switching	Pending	CA04/000949	PCT	Saed, Aryan	6/23/04
ICE-015	Adaptive Predistortion for a Transmit System	Allowed	10/613,372	U.S.A.	Saed, Aryan	7/3/2003
ICE-015CP	Adaptive Predistortion for a Transmit System (CIP)	Allowed	10/641,372	U.S.A.	Saed, Aryan	8/13/2003

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-016	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining	Issued	6,879,209	U.S.A.	Grundinagh, Johan	7/8/2003
ICE-016CP	Switched-Mode Power Amplifier Using Lumped Element Impedance Inverter for Parallel Combining (CIP)	Pending	11/099,916	U.S.A.	Grundinagh, Johan	4/6/2005
ICE-017	Integrated Circuit Incorporating Wire Bond Inductance	Pending	10/610,497	U.S.A.	Wight, James	6/30/2003
ICE-018	Digital Branch Calibrator for An RF Transmitter	Pending	10/627,881	U.S.A.	Saed, Aryan	7/25/2003
ICE-019	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments	Allowed	10/613,856	U.S.A.	Saed, Aryan	7/3/2003
ICE-019PC	Adaptive Predistortion for a Transmit System with Gain,	Pending	CA04/000972	PCT	Saed, Aryan	6/30/2004

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
	Phase and Delay Adjustments					
ICE-019CP1.	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,371	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP2	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,374	U.S.A.	Saed, Aryan	8/13/2003
ICE-019CP3	Adaptive Predistortion for a Transmit System with Gain, Phase and Delay Adjustments (CIP)	Allowed	10/641,373	U.S.A.	Saed, Aryan	8/13/2003
ICE-020	Staggered AGC with Digitally Controlled VGA	Pending	10/661,945	U.S.A.	Birkett, Neil	9/12/2003
ICE-020PC	Staggered AGC with Digitally Controlled VGA	Pending	CA04/001566	PCT	Birkett, Neil	8/26/2004
ICE-021	Optimized FFT/IFFT Module	Pending	10/662,063	U.S.A.	Amer, Maher	9/12/2003
ICE-022	Method for Amplitude Insensitive Packet Detection	Pending	10/661,943	U.S.A.	Birkett, Neil	9/12/2003

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-022PC	Method for Amplitude Inensitive Packet Detection	Pending	CA04/001565	PCT	Rikett, Neil	8/26/2004
ICE-023	Frequency Domain Equalizer for Wireless Communications System	Pending	10/661,147	U.S.A.	Saad, Aryan	9/12/2003
ICE-023PC	Frequency Domain Equalizer for Wireless Communications System	Pending	CA04/001564	PCT	Saad, Aryan	8/26/04
ICE-029	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Pending	10/779,322	U.S.A.	Wight, James	2/13/2004
ICE-029PC	Methods and Systems for Signal Amplification Through Envelope Removal and Restoration	Pending	CA05/000153	PCT	Wight, James	2/7/2005
ICE-030	Systems and Methods for Rapid Signal Detection and Identification	Pending	10/833,170	U.S.A.	Mohr, Michael L.	07/01/2004
ICE-031	Multiple Input, Multiple Output Communications Systems	Pending	10/884,633	U.S.A.	Wight, James	07/02/2004

<u>Item</u>	<u>Title</u>	<u>Status</u>	<u>Number</u>	<u>Country</u>	<u>Inventor</u>	<u>Filing Date</u>
ICE-031C1	Multiple Input, Multiple Output Communications Systems (CIP)	Pending	10/954,429	U.S.A.	Wight, James	09/30/2004
ICE-032	Power Amplifier	Pending	10/884,627	U.S.A.	Parker, Kevin	7/02/2004
ICE-033PR	Improved Power Amplifier and Related Methods.	Expired	60/325,301	U.S.A.	Grundlingh, Johan	11/05/2004
ICE-033	Power Amplifier	Pending	No yet assigned	U.S.A.	Grundlingh, Johan	11/07/2005

Assignor represents, warrants and covenants (except that Purchaser makes no representation, warranty or covenant with respect to the entries in the above chart that are Australian patent applications with Item designations ending in "AU**") that:

- (1) Assignor has the full power and authority, and has obtained all third party consents, approvals and/or other authorizations required, to enter into this Agreement, make the assignments, and to carry out its obligations under this Assignment of Patent Rights;
- (2) Assignor owns all right, title, and interest to the Patent Rights, including, without limitation, all right, title, and interest to sue for infringement of the Patent Rights. Assignor has obtained and properly recorded previously executed assignments for the Patent Rights as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction. The Patent Rights are free and clear of all liens, claims, mortgages, security interests or other encumbrances, and restrictions. There are no actions, suits, investigations, claims or proceedings threatened, pending or in progress relating in any way to the Patent Rights. There are no existing contracts, agreements, options, commitments, proposals, bids, offers, or rights with, to, or in any person to acquire any of the Patent Rights.

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Assignor shall, at the reasonable request of Assignee and without demanding any further consideration therefor, do all things necessary, proper, or advisable, including without limitation the execution, acknowledgment and recordation of specific assignments, oaths, declarations and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights. Such assistance shall include providing, and obtaining from the respective inventors, prompt production of pertinent facts and documents, giving of testimony, execution of petitions, oaths, powers of attorney, specifications, declarations or other papers and other assistance reasonably necessary for filing patent applications, complying with any duty of disclosure, and conducting prosecution, reexamination, reissue, interference or other priority proceedings, opposition proceedings, cancellation proceedings, public use proceedings, infringement or other court actions and the like with respect to the Patent Rights. With prior written approval by Assignee, Assignee will pay Assignor's reasonable costs and expenses.

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Exhibit B

The terms and conditions of this Assignment of Patent Rights shall inure to the benefit of Assignee, its successors, assigns and other legal representatives, and shall be binding upon Assignor, its successor, assigns and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at _____

on _____
ASSIGNORBy: Michelle F. SchianoName: Michelle F. SchianoTitle: Director

(Signature MUST be notarized)

STATE OF Massachusetts)
COUNTY OF Bristol) ss.

Christine Cunningham

On March 15, 2006 before me, Michelle Cunningham, Notary Public in and for said State, personally appeared Michelle F. Schiano, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.

Signature

